

SUBSTITUTE FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.
ANCO/89US

SERIAL NO.
10/679,873

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use several sheets if necessary)

APPLICANT
Robert Everest Johnson et al.

Confirmation No.
3837

FILING DATE
October 6, 2003

GROUP
2817

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.A						
	A.B						
	A.C						
	A.D						
	A.E						
	A.F						
	A.G						
	A.H						
	A.I						
	A.J						
	A.K						

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION (YES/NO)
H.C.	A.L	EP 0 367 457 B1	27.03.1996	EPO	H03F	1/32	Yes
H.C.	A.M	EP 0 948 131 B1	27.03.2002	EPO	H03F	1/32	Yes
H.C.	A.N	EP 0 998 026 A1	03.05.2000	EPO	H03F	1/32	Yes
H.C.	A.O	WO 97/08822	06.03.1997	WIPO	H03F	1/32	Yes
	A.P						
	A.Q						

OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)

H.C.	A.R	Lohtia, Anit et al., "Power Amplifier Linearization using Cubic Spline Interpolation", <u>IEEE</u> , (1993), No. 0-7803-1266-x/93, pp. 676-679.					
H.C.	A.S	Stapleton, Shawn P., "Amplifier Linearization Using Adaptive Digital Predistortion - The need for greater linearity can be addressed at the digital coding level", <u>Applied Microwave & Wireless</u> , Technical Feature, (February 2001), pp.72-77.					
H.C.	A.T	Vella-Coleiro, George, <u>Frequency-Dependent Phase Pre-Distortion for Reducing Spurious Emissions in Communication Networks</u> , U.S. Patent Application Serial No. 10/068,343, filed on February 5, 2002.					

EXAMINER

Henry choe

DATE CONSIDERED

3/11/05

EXAMINER: Initial if citation considered, whether or not in conformance. Draw line through citation only if not in conformance and not considered. Include a copy of this form with next communication to applicant.



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Architecture and Implementation Methods of Digital Predistortion Circuitry																																																																																
<p>Application Number: 10/679873</p> <p>Confirmation Number: 3837</p> <p>First Named Applicant: Robert Johnson</p> <p>Attorney Docket Number: ANCO89USA</p> <p>Art Unit: 2817</p> <p>Search string: (3241078 or 3689752 or 4156283 or 4879519 or 4978873 or 5023565 or 5049832 or 5115409 or 5130663 or 5323119 or 5325095 or 5414383 or 5477187 or 5485120 or 5491454 or 5528196 or 5617061 or 5621354 or 5691668 or 5732333 or 5760646 or 5808512 or 5831478 or 5862459 or 5867065 or 5877653 or 5892397 or 5900778 or 5912586 or 5923214 or 5929703 or 5933766 or 5959499 or 5986499 or 6046635 or 6052023 or 6072364 or 6091295 or 6091715 or 6104239 or 6118335 or 6118339 or 6125266 or 6137335 or 6141390 or 6144255 or 6148185 or 6154641 or 6157253 or 6208846).pn.</p>																																																																																	
<h3>US Patent Documents</h3> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>4c.</td><td>1</td><td>3241078</td><td>1966-03-15</td><td>H.E. Jones</td><td></td><td>329</td><td>50</td></tr><tr><td></td><td>2</td><td>3689752</td><td>1972-09-05</td><td>Gilbert</td><td></td><td>235</td><td>194</td></tr><tr><td></td><td>3</td><td>4156283</td><td>1979-05-22</td><td>Gilbert</td><td></td><td>364</td><td>841</td></tr><tr><td></td><td>4</td><td>4879519</td><td>1989-11-07</td><td>Myer</td><td></td><td>330</td><td>149</td></tr><tr><td></td><td>5</td><td>4978873</td><td>1990-12-18</td><td>Shoemaker</td><td></td><td>307</td><td>498</td></tr><tr><td></td><td>6</td><td>5023565</td><td>1991-06-11</td><td>Lieu</td><td></td><td>330</td><td>151</td></tr><tr><td></td><td>7</td><td>5049832</td><td>1991-09-17</td><td>Carvers</td><td></td><td>330</td><td>149</td></tr><tr><td></td><td>8</td><td>5115409</td><td>1992-05-19</td><td>Stepp</td><td></td><td>364</td><td>841</td></tr><tr><td></td><td>9</td><td>5130663</td><td>1992-07-14</td><td>Tattersall, Jr.</td><td></td><td>330</td><td>52</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	4c.	1	3241078	1966-03-15	H.E. Jones		329	50		2	3689752	1972-09-05	Gilbert		235	194		3	4156283	1979-05-22	Gilbert		364	841		4	4879519	1989-11-07	Myer		330	149		5	4978873	1990-12-18	Shoemaker		307	498		6	5023565	1991-06-11	Lieu		330	151		7	5049832	1991-09-17	Carvers		330	149		8	5115409	1992-05-19	Stepp		364	841		9	5130663	1992-07-14	Tattersall, Jr.		330	52
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10	5323119	1994-06-21	Powell et al.
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<input checked="" type="checkbox"/>	50	6208846	2001-03-27	Chen et al.	B1	455	127

Remarks

Note: Remarks are not for responding to an office action.

Two Electronic Information Disclosure Statements containing a total of 80 US Patent references are being submitted this date. In addition, an Information Disclosure Statement containing four Foreign Patent and seven Other Document references is being mailed on even date herewith.

Signature

Examiner Name	Date
Henry che	3/11/05

SUBSTITUTE FORM PTO-1449 (MODIFIED) INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. ANCO/89US	SERIAL NO. 10/679,873
	APPLICANT Robert Everest Johnson et al.			
	FILING DATE October 6, 2003		GROUP 2817	

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	A.K						

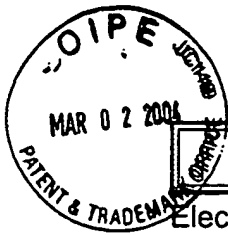
FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

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H-c.	A.L	EP 1 162 732 A3	12.12.2001	EPO	H03F	1/32	
H-c.	A.M	GB 2 400 509	10.13.2004	Great Britain	H03F	1/32	
H-c.	A.N	WO 02/095932	11.28.2002	International	H03F	1/26	
	A.O						
	A.P						
	A.Q						

OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)

H-c.	A.R	US2003/0146787 A1, U.S. Patent Application to Hedberg et al. Filed February 6, 2002, POWER AMPLIFIER LINEARIZER THAT COMPENSATES FOR LONG-TIME-CONSTANT MEMORY EFFECTS AND METHOD THEREFOR
H-c.	A.S	British Search Report, November 29, 2004
H-c.	A.T	US2003/0058959 A1, U.S. Patent Application to Rafie et al. Filed September 25, 2001, COMBINED DIGITAL ADAPTIVE PRE-DISTORTER AND PRE-EQUALIZER SYSTEM FOR MODEMS IN LINK HOPPING RADIO NETWORKS
EXAMINER	DATE CONSIDERED 3/11/05 <i>Henry choe</i>	

EXAMINER: Initial if citation considered, whether or not in conformance. Draw line through citation only if not in conformance and not considered. Include a copy of this form with next communication to applicant.



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

Architecture and Implementation Methods of Digital
Predistortion Circuitry

Application Number: 10/679873



Confirmation Number: 3837

First Named Applicant: Robert Johnson

Attorney Docket Number: ANCO89USB

Art Unit: 2817

Search string: (6211733 or 6236267 or 6236837 or 6275685
or 6285251 or 6285255 or 6304140 or 6337599
or 6342810 or 6353360 or 6356146 or 6359508
or 6377785 or 6414545 or 6512417 or 5119040
or 5594385 or 5610554 or 5420536 or 5740520
or 5818298 or 5880633 or 5898338 or 5949283
or 5977826 or 6166601 or 6172564 or 6388518
or 6496064 or 65535066).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
4.	1	6211733	2001-04-03	Gentzler	B1	330	149
	2	6236267	2001-05-22	Anzil	B1	330	149
	3	6236837	2001-05-22	Midya	B1	455	63
	4	6275685	2001-08-14	Wessel et al.	B1	455	126
	5	6285251	2001-09-04	Dent et al.	B1	330	127
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	10	6353360	2002-03-05	Hau et al.	B1	330	149
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	29	6496064	2002-12-17	Rzyski	B2	330	149
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Signature

Examiner Name	Date
Henry chae	3/11/05